

<b>Notice of References Cited</b>	Application/Control No. 10/579,383		Applicant(s)/Patent Under Reexamination KULKARNI ET AL.	
	Examiner Paul R. Myers		Art Unit 2111	Page 1 of 1

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#### NON-PATENT DOCUMENTS

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	U	"Reordering Memory Bus Transactions for Reduced PowerConsumption" by Childers et al pages 1-9 June 2000
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.